

IN THE CLAIMS:

Please amend the claims as shown below. The claims, as pending in the subject application, read as follows:

1. (Currently Amended) An image processing apparatus comprising:

a bit connection component that connects ~~a cumulative value of decimals of preceding image data~~ a decimal portion of image data of a preceding pixel to ~~input~~ image data of a target pixel as lower bits of the ~~input~~ image data of the target pixel;

a correction component that ~~corrects the connected input image data with a quantization error~~ generates corrected image data by adding a correction value to the bit-connected image data of the target pixel;

a latch component that latches a decimal portion of the corrected ~~input~~ image data to be connected to image data of a next pixel;

a quantization component that quantizes an integral portion of the corrected ~~input~~ image data;

a calculation component that calculates the quantization error, which is generated by quantization by said quantization component, ~~to provide the calculated quantization error to said correction component~~;

a buffer that stores the calculated quantization error; and

an error diffusion component that generates the correction value to be added to input data of the next pixel by diffusing ~~diffuses~~ the quantization error ~~on the basis of at least a quantization error of a first pixel, which is stored in said buffer.~~

2. (Canceled)

3. (Previously Presented) The apparatus according to claim 1, further comprising a stop component that stops propagating the correction value in a case in which it is inappropriate to propagate the correction value to next and subsequent pixels.

4. (Previously Presented) The apparatus according to claim 1, further comprising:

a clear component that clears the decimal portion held in said latch component in a case in which it is inappropriate to connect the decimal portion of the correction value, which is latched in said latching component, to the lower bit side of the next input image data.

5. (Original) The apparatus according to claim 4, further comprising a processing limit component that limits clearing by said clear component when a scanning direction of the input image is reversed.

6. (Original) The apparatus according to claim 3, wherein the case in which it is inappropriate to propagate the correction value to next and subsequent pixels includes at least one of a case in which a pixel of interest is a start pixel of a line, a case in which the pixel of interest has a value equal to a lower limit level of the input image, and a case in which the pixel of interest has a value equal to an upper limit level of the input image.

7. (Original) The apparatus according to claim 1, further comprising a numerical value limit component that limits the quantization error calculated by said calculation component to a numerical value within a predetermined range.

8. to 19. (Canceled)

20. (Currently Amended) A method for an ~~An~~ image processing ~~method~~ apparatus, comprising ~~the steps of~~:

a bit connection step of connecting a ~~cumulative value of decimal~~[[s]] portion of image data of a preceding pixel to image data of a target pixel as lower bits of ~~the to input image data as lower bits of the input image data of the target pixel~~;

a correction step of generating corrected ~~correcting the connected input~~ image data with a quantization error by adding a correction value to the bit-connected ~~image data of the target pixel~~;

a latch step of latching a decimal portion of the corrected ~~input~~ image data to be connected to the image data of the next pixel;

a quantization step of quantizing an integral portion of the corrected ~~input~~ image data;

a calculation step of calculating the quantization error, which is generated ~~in said quantization step, to provide the calculated quantization error to the correcting step~~;

a storing step of storing the calculated quantization error in a buffer; and

an error diffusion step of generating the correction value to be added to input data of the next pixel by diffusing the quantization error on the basis of at least a quantization error of a first pixel, which is stored in said buffer.

21. (Canceled)

22. (Previously Presented) The method according to claim 20, further comprising a step of stopping propagation of the correction value in a case in which it is inappropriate to propagate the correction value to next and subsequent pixels.

23. (Previously Presented) The method according to claim 20, further comprising the step of:

clearing the decimal portion held in said latching step in a case in which it is inappropriate to connect the decimal portion of the correction value, which is latched in said latching step, to the lower bit side of the next input image data.

24. (Previously Presented) The method according to claim 23, further comprising a step of limiting the clear process of said clear step when a scanning direction of the input image is reversed.

25. (Original) The method according to claim 22, wherein the case in which it is inappropriate to propagate the correction value to next and subsequent pixels includes at least one of a case in which a pixel of interest is a start pixel of a line, a case in

which the pixel of interest has a value equal to a lower limit level of the input image, and a case in which the pixel of interest has a value equal to an upper limit level of the input image.

26. (Previously Presented) The method according to claim 20, further comprising a step of limiting the quantization error calculated in said calculation step to a numerical value within a predetermined range.

27. to 38. (Canceled)

39. (Currently Amended) A computer-readable storage medium on which is stored a computer-executable program for a method executed by an image processing method apparatus, the method program comprising the steps of:

a bit connection step of connecting a ~~cumulative value of decimal~~[[s]] portion of image data of a preceding pixel to image data of a target pixel as lower bits of the ~~to input~~ image data as lower bits of the input image data of the target pixel;

a correction step of generating corrected ~~correcting the connected input~~ image data with a quantization error by adding a correction value to the bit-connected image data of the target pixel;

a latch step of latching a decimal portion of the corrected ~~input~~ image data to be connected to the image data of the next pixel;

a quantization step of quantizing an integral portion of the corrected ~~input~~ image data;

a calculation step of calculating the quantization error, which is generated  
in said quantization step ~~to provide the calculated quantization error to said step of~~  
~~correcting;~~

a storing step of storing the calculated quantization error in a buffer; and  
an error diffusion step of generating the correction value to be added to  
input data of the next pixel by diffusing the quantization error on the basis of at least a  
~~quantization error of a first pixel, which is stored in said buffer.~~

40. to 61. (Canceled)